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IN THE UNITED	STATES PATENT	AND TRADEMARK OFFICE
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Priority Application Serial No.	09/468,247
Priority Filing Date	12/20/99
Priority Filing Date	Perino et al
Inventor	Dambus Inc
Applicant	Ramous, mc.
Descriptive Group Art Unit	
D. L. E. Sansinger	Paulilen
Attorney's Docket No.	RB1-035USC3
Title: Chip Socket Assembly and Chip File Assembly for	Semiconductor Chips
Title: Chip Socket Assembly and Chip File Assembly for	Semiconductor emps

# PRELIMINARY AMENDMENT

To:

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Commissioner of Patents and Trademarks,

Washington, D.C. 20231

From:

Daniel L. Hayes (Tel. 509-324-9256; Fax 509-323-8979)

Customer No. 29501

#### <u>AMENDMENTS</u>

### In the Specification:

On page 2, prior to the heading "FIELD OF THE INVENTION", please insert the following:

# --RELATED APPLICATIONS

This is a continuation of U.S. Patent Application Serial No. 09/468,247, filed December 20, 1999, which is now U.S. Patent No.\_

# In the Claims:

Please cancel claims 1-67.

Please add the following new claims 68-90.

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